

AMENDMENTS TO THE CLAIMS

1.-2. (Canceled)

3. (Currently Amended) A high efficiency amplifier including input signal dividing means for splitting an input ~~signal;~~signal, comprising:

a first amplifier for amplifying a first input signal fed from said input signal dividing means;

a second amplifier for amplifying, when power of a second input signal fed from said input signal dividing means is greater than specified power, the second input ~~signal; and~~signal;

a combining circuit for combining an output signal of said first amplifier and an output signal of said second amplifier, said high efficiency amplifier comprising before said first ~~amplifier; amplifier; and~~

a level limiting circuit for limiting, when amplitude of the first input signal fed from said input signal dividing means is greater than a specified level, the amplitude of the first input signal to less than the specified level, and for supplying to said first amplifier.

4. (Original) The high efficiency amplifier according to claim 3, wherein said level limiting circuit is a limiter circuit.

5. (Original) The high efficiency amplifier according to claim 3, wherein said level limiting circuit is a driver amplifier having a saturation characteristic.

6. (Original) The high efficiency amplifier according to claim 3, wherein said level limiting circuit is a waveform shaping circuit.

7. (Currently Amended) The high efficiency amplifier according to claim 6, further comprising:

before said second amplifier a waveform shaping circuit that suppresses output of a signal when amplitude of the second input signal fed from said input signal dividing means is less than a specified level.

8. (Currently Amended) The high efficiency amplifier according to claim 7, further comprising:

a first distortion compensation circuit for compensating for nonlinear distortion of said first amplifier before said first amplifier; and

a second distortion compensation circuit for compensating for nonlinear distortion of said second amplifier before said second amplifier.

9. (Currently Amended) The high efficiency amplifier according to claim 8, further comprising:

a first adaptive control circuit for varying parameters of said first distortion compensation circuit in response to the output signal of said first amplifier; and

a second adaptive control circuit for varying parameters of said second distortion compensation circuit in response to the output signal of said second amplifier.

10. (Currently Amended) A high efficiency amplifier including input signal dividing means for splitting an input signal; ~~signal, comprising:~~

a first amplifier for amplifying a first input signal fed from said input signal dividing means;

a second amplifier for amplifying a second input signal fed from said input signal dividing means; and

a combining circuit for combining an output signal of said first amplifier and an output signal of said second amplifier, said high efficiency amplifier ~~comprising:~~ including:

a first waveform shaping circuit before said first amplifier, said first waveform shaping circuit limiting, when amplitude of the first input signal fed from said input signal dividing

means is greater than a specified level, the amplitude of the first input signal to less than a specified level, and supplying to said first amplifier; and

a second waveform shaping circuit before said second amplifier, said second waveform shaping circuit suppressing output of a signal when amplitude of the second input signal fed from said input signal dividing means is less than a specified level.

11. (Original) The high efficiency amplifier according to claim 10, wherein said first waveform shaping circuit has a characteristic of gradually limiting the amplitude of the input signal as the amplitude of the input signal approaches the specified level.

12.-13. (Canceled)